

<b>Notice of References Cited</b>	Application/Control No. 09/942,001	Applicant(s)/Patent Under Reexamination STEINBUSCH ET AL.	
	Examiner Mary J. Steelman	Art Unit 2122	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,768,593	06-1998	Walters et al.	717/141
*	B	US-5,872,978	02-1999	Hoskins, Asher J.	717/158
*	C	US-6,546,550 B1	04-2003	Ogata et al.	717/148
*	D	US-6,513,156 B2	01-2003	Bak et al.	717/151
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Gokhale, Maya, Stone, Janice M., "NAPA C: Compiling for a Hybrid RISC/FPGA Architecture", 1998, IEEE, retrieved 08/04/2004.
	V	Hsieh, Cheng-Hseueh A., Conte, Marie T., Johnson, Tereas L., Gyllenhaal, John C., Hwu, Wen-mei W., "A Study of the Cache and Branch Performance Issues with Running Java on Current Hardware Platforms", p. 211-216, IEEE 1997, retrieved 08/04/2004.
	W	Newhall, Tia, Miller, Barton P., "Performance Measurement of Dynamically Compiled Java Executions", p. 42-50, ACM 1999, retrieved 08/04/2004.
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.